

FIG. 1

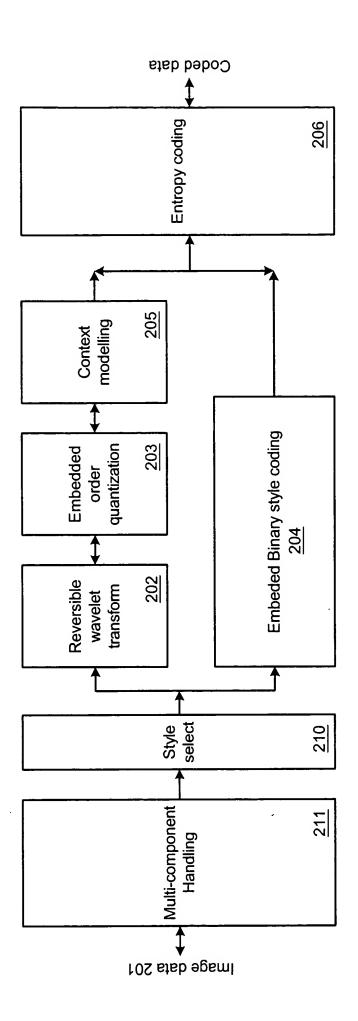


FIG. 2

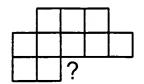


FIG. 3

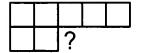
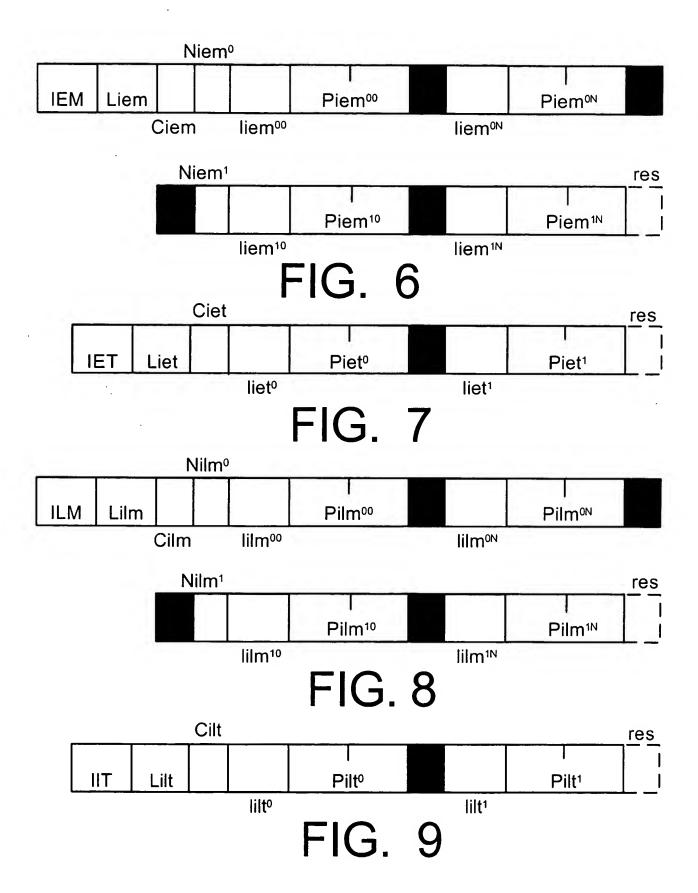


FIG. 4

0	1	2	3	4	5
6	7	8	9	10	11
12	13	14	15	16	17
18	19	20	21	22	23
24	25	26	27	28	29
30	31	32	33	34	35
36	37	38	39	40	41

FIG. 5



b	b+2	b+2
b+2	b+3	0,2
b+2		b+3

FIG. 10

	16			
16	8	8	4	
8	3	4	4	
		1	2	2
	2			1

FIG. 11

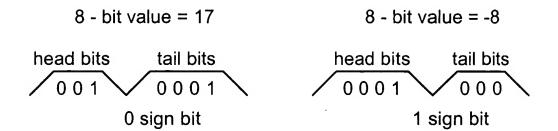
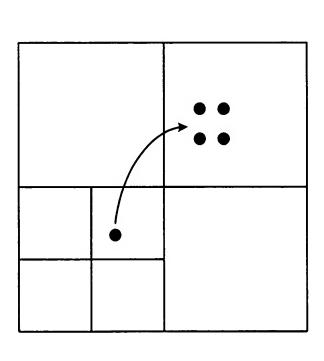


FIG. 12

NW	Ν	NE
W	Р	Е
sw	S	SE

FIG. 13



Child-based 2x2 block order

FIG. 14

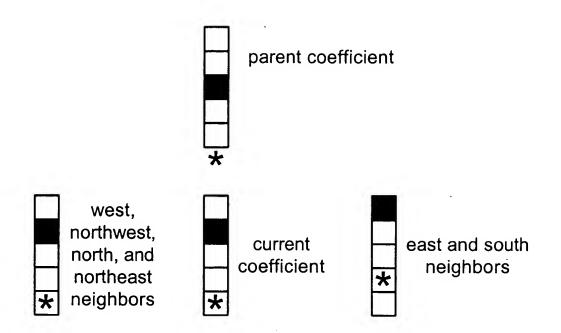


FIG. 15

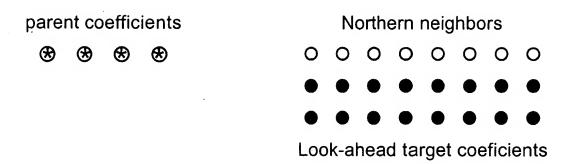


FIG. 16

Туре А	Target	Туре В
	Туре В	

Coefficient contribution

West 2 bits
East 1 bit
South 1 bit

FIG. 17

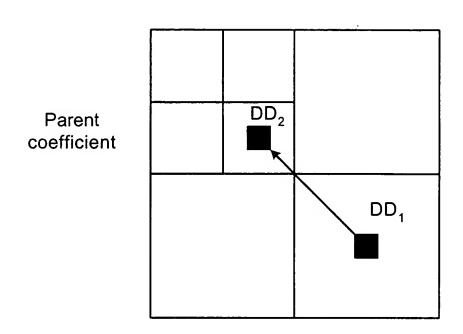


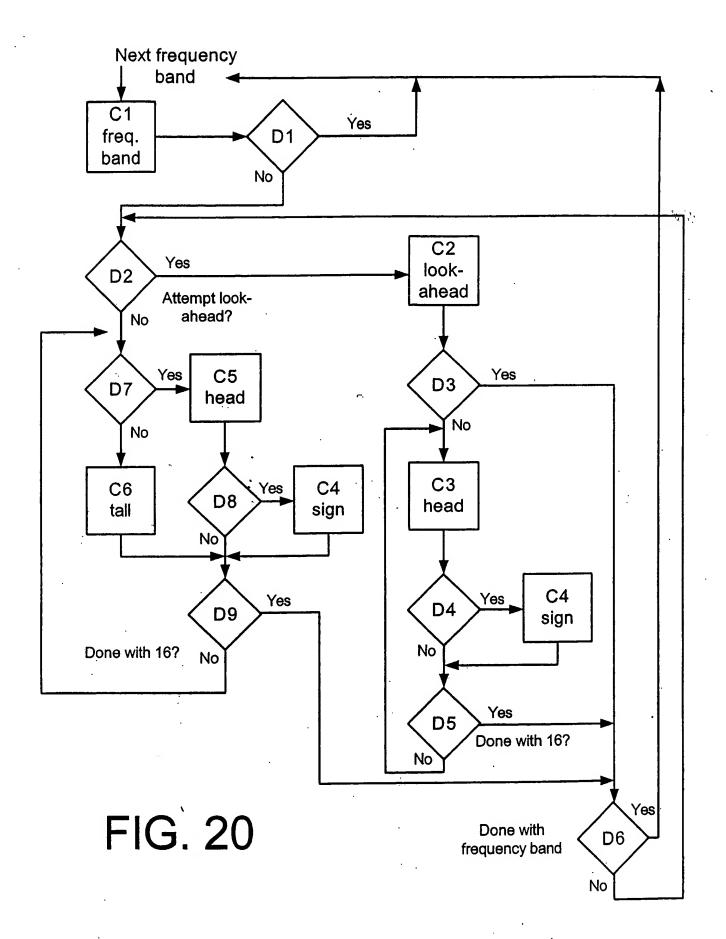
FIG. 18

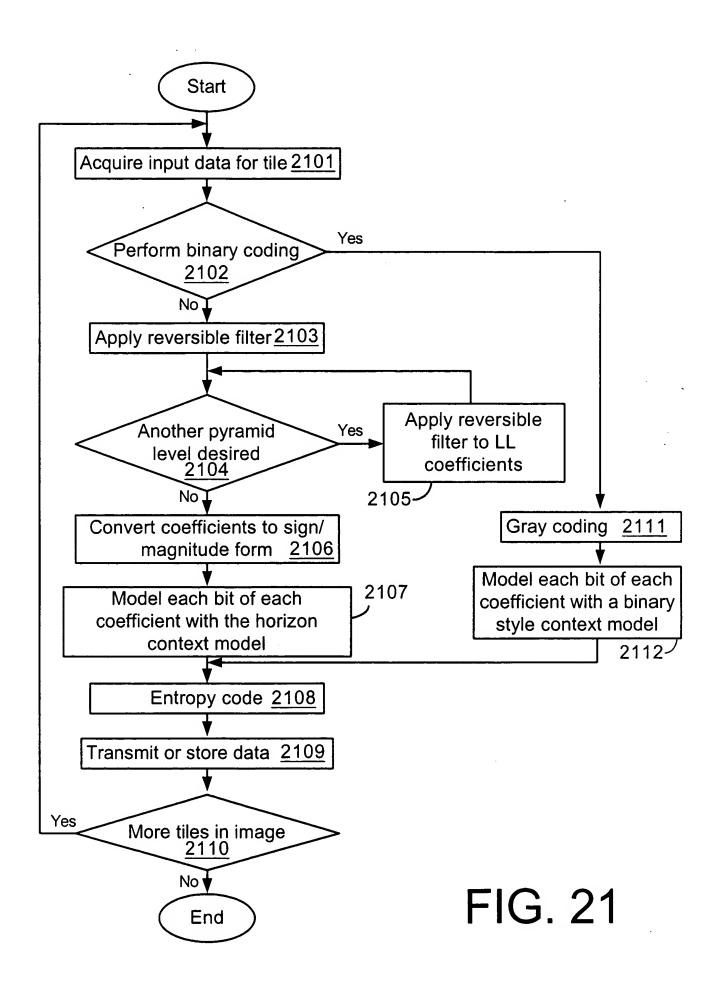
Parent Type A

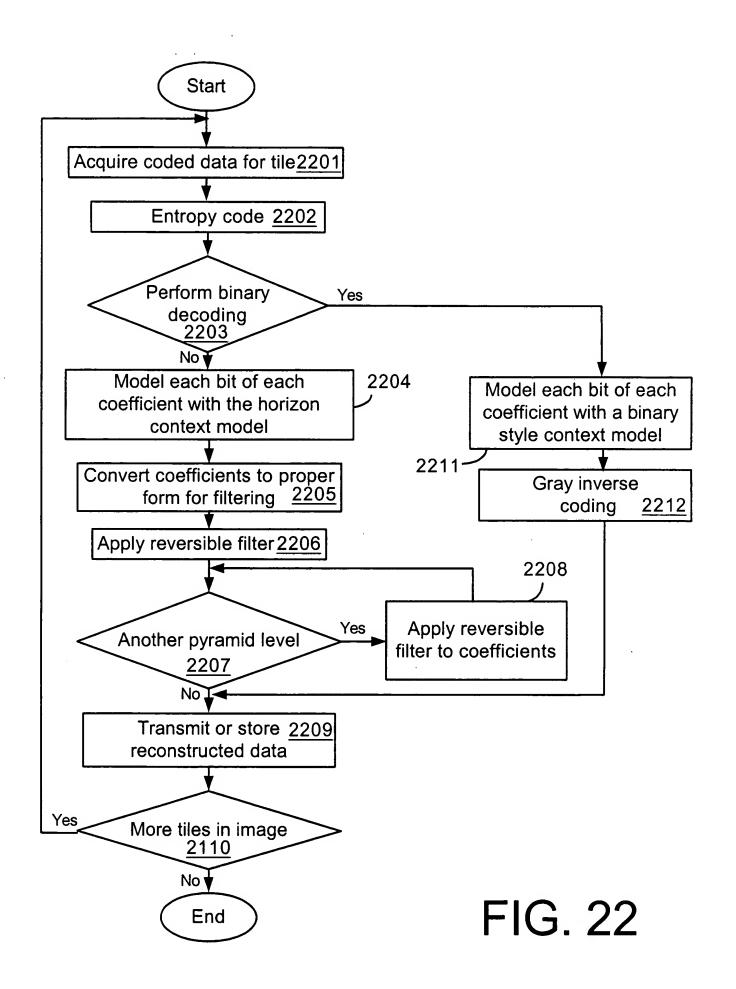
	Type B	
Type A	Target	Type B
Type B	Type A	

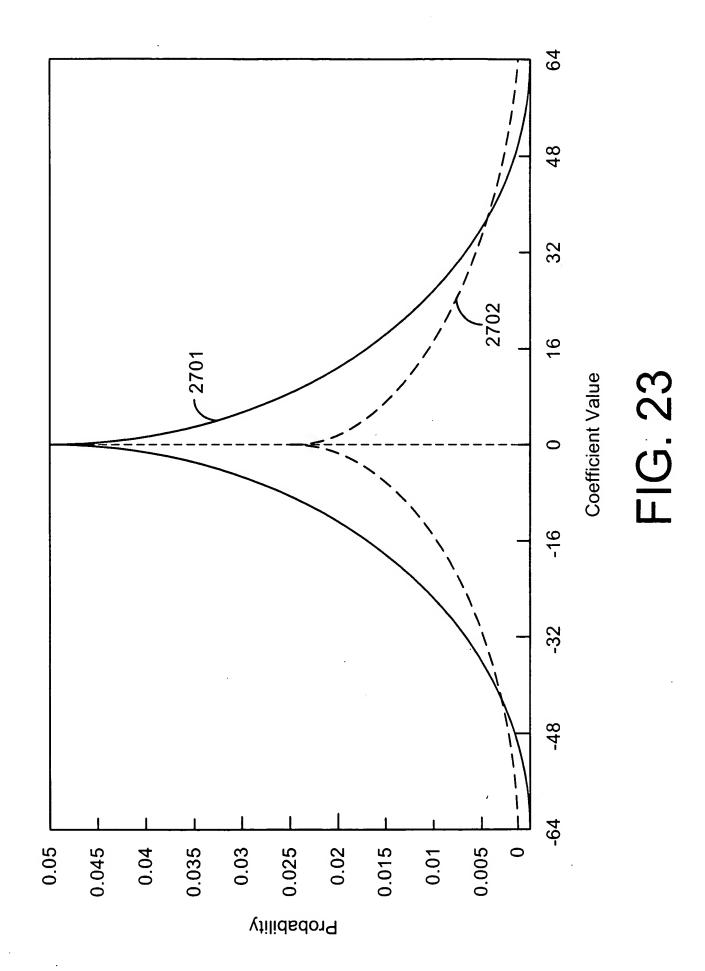
Coefficient contribution
Northwest 1 bit
North 2 bits
West 2 bits
East 1 bit
South 1 bit
Parent 2 bits

FIG. 19









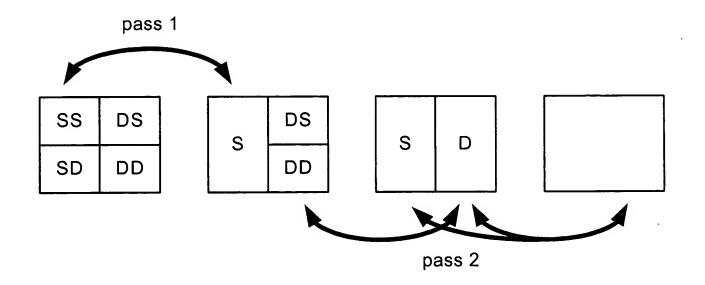


FIG. 24

			-3
		-3	22
	-3	22	0
-3	22	0	-22
22	0	-22	.3
0 22	-22 0	3 -22	6.
			8.

FIG. 25

			-3
		-3	22
		22, 3	0
		-22	-22, 3
22, -3	22		
0 22, -3	-22, -3 22		

FIG. 26

	-3	-3	
-3	22	22	£-
3	-22	-22	က
	3	3	

FIG. 27



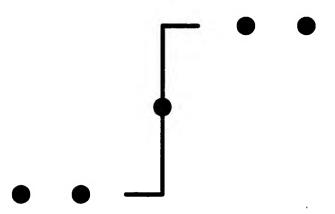


FIG. 28B

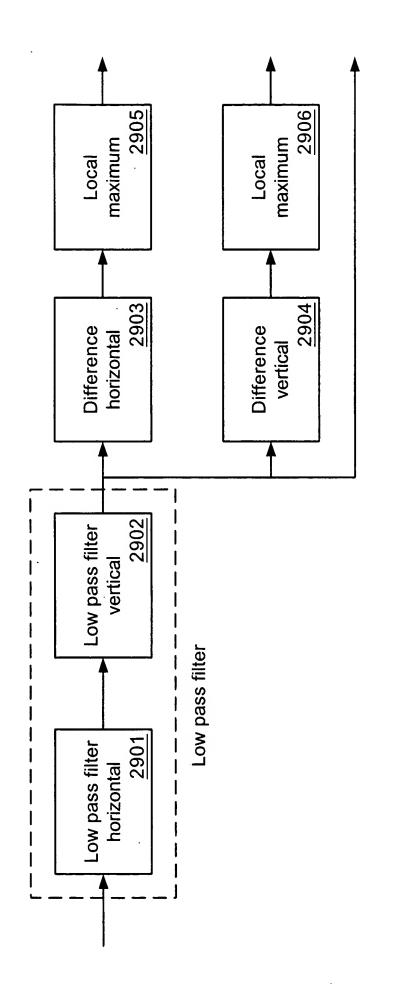


FIG. 29

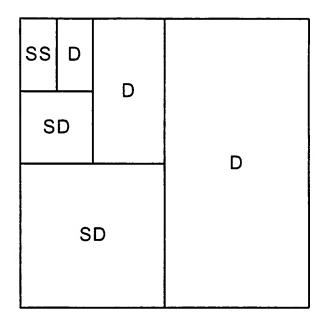


FIG. 30

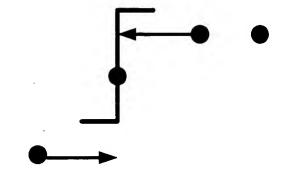
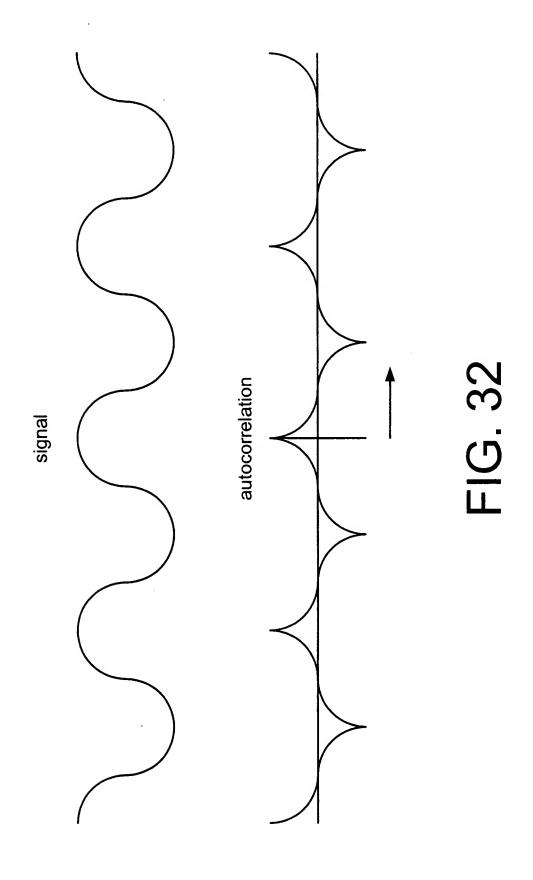


FIG. 31



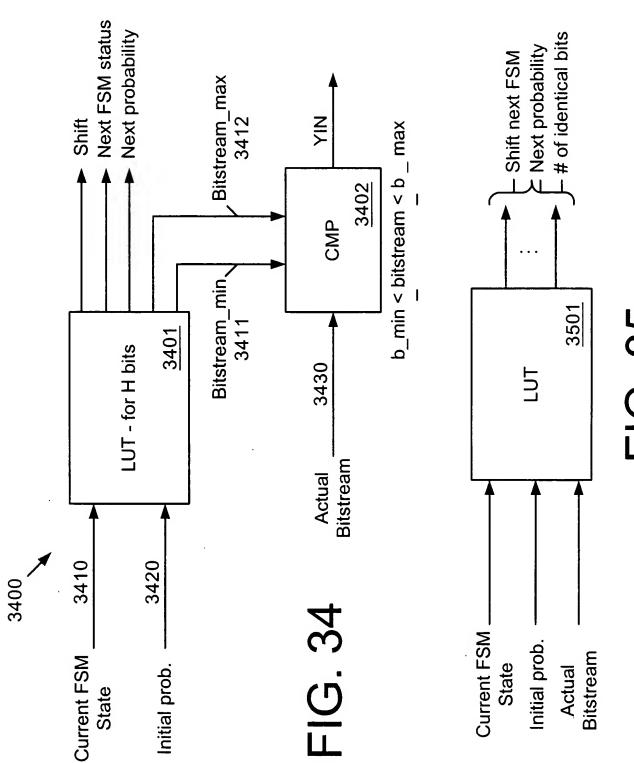


FIG. 35

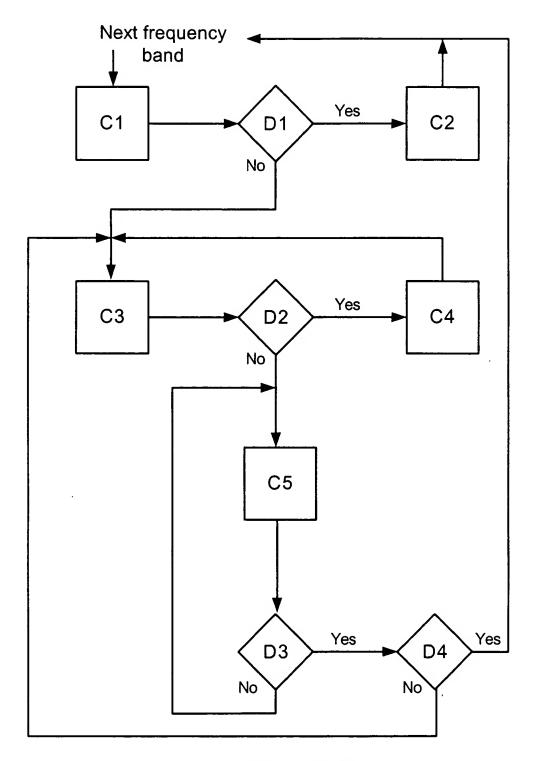


FIG. 36

X	Х	Х	Х	Х
X	Х	Target		

FIG. 37

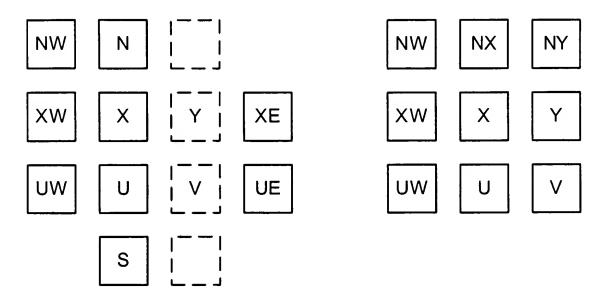


FIG. 38A

FIG. 38B

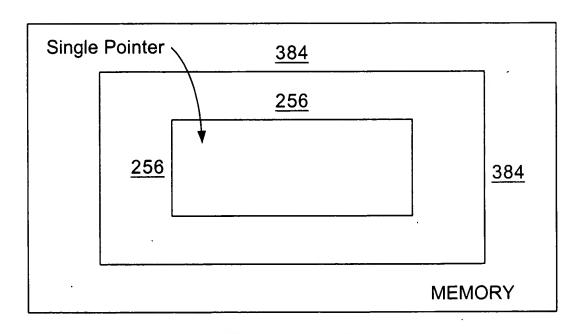


FIG. 33

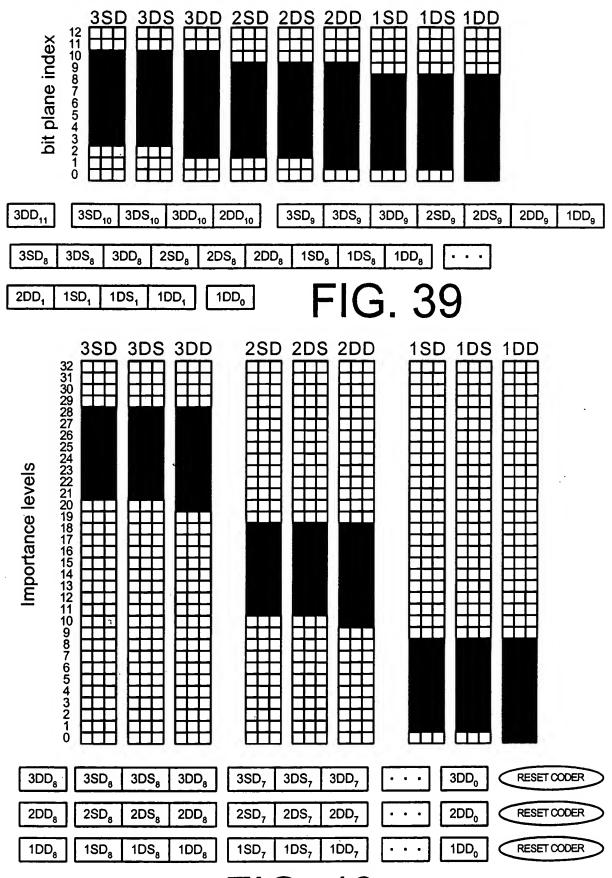


FIG. 40

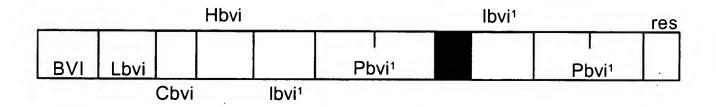


FIG. 41